U.S. Department of Commerce, Patent and Trademark Office		Atty Docket No.		Serial No.
		M-5648-1D US		09/458,551
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Applicant(s)		
(Use several sheets if necessary)		Alexander Joffe et al.		
		Filing Date		Group
		December 9, 1999		2739

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*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
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	AB						RECEIVED
	AC						
	AD						NOV 0 6 2002
	AE						Technology Center 2800
	AF						
	AG						RECEIVED
	AH						
	AI						NOV 0 7 2002
	AJ						Technology Center 2100
	AK						

Foreign Patent Documents

							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
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_MJ_	AP	Farrens M. K. et al.: "Strategies For Achieving Improved Processor Throughput", Computer Architecture News, Association For Computing Machinery, New York, US, vol. 19, no. 3, 1 May 1991 (1991-05-01), pages 362-369.
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Examiner _Majid Banankhah_ | Date Considered _2/27/04_

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U.S. Department of Commerce, Patent and Trademark Office	MAY 1 0 2000 Docket No.					Serial No.	
	M-5648-1D US					09/458,551	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Applicant(s)						
(Use several sheets if necessary)	Joffe, Alexander; Vhshetsky, Dmitry						
	Filing Date					Group	
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	AO							
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μβ	AH	5,517,495	05/14/96	Lund et al.	370	60	
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μβ	AK	5,555,264	09/10/96	Sallberg et al.	370	17	

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μβ	AM	WO	11/30/95	PCT				
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μβ	AR	Alan Demers et al. "Analysis and Simulation Of A Fair Queueing Algorithm" ©1989 ACM, pp 1-12.
μβ	AS	"ATMS2003B Switch Controller 1 "White"", MMC Networks, December 1995, 32 pages.

Examiner _Maju Baaanku_ Date Considered _2/27/04_

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Mβ	AS	Flanagan, William A., "ATM (Asyncrhronous Transfer Mode) User's Guide," (Flatiron Publishing, Inc. New York, 1994), pp. 25-49.

Examiner _Mojil Bmahllal_ | Date Considered _2/27/04_

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Examiner *Mohammad Bawahdlah* | Date Considered *2/27/04*

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		M-5648-1D U.S.	Unassigned
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µß	AQ	Tanenbaum, Andrew. S., "Computer Networks" (Prentice Hall PTR, 3rd Ed., 1996) pp. 148-150.
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Examiner *Lloyd Barakbah* | Date Considered *2/27/04*

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